

VG-469

REFERENCE DESIGN #6

DESKTOP IMPLEMENTATION

INTERNAL VOLTAGE SENSE
NO DMA
NO P&P

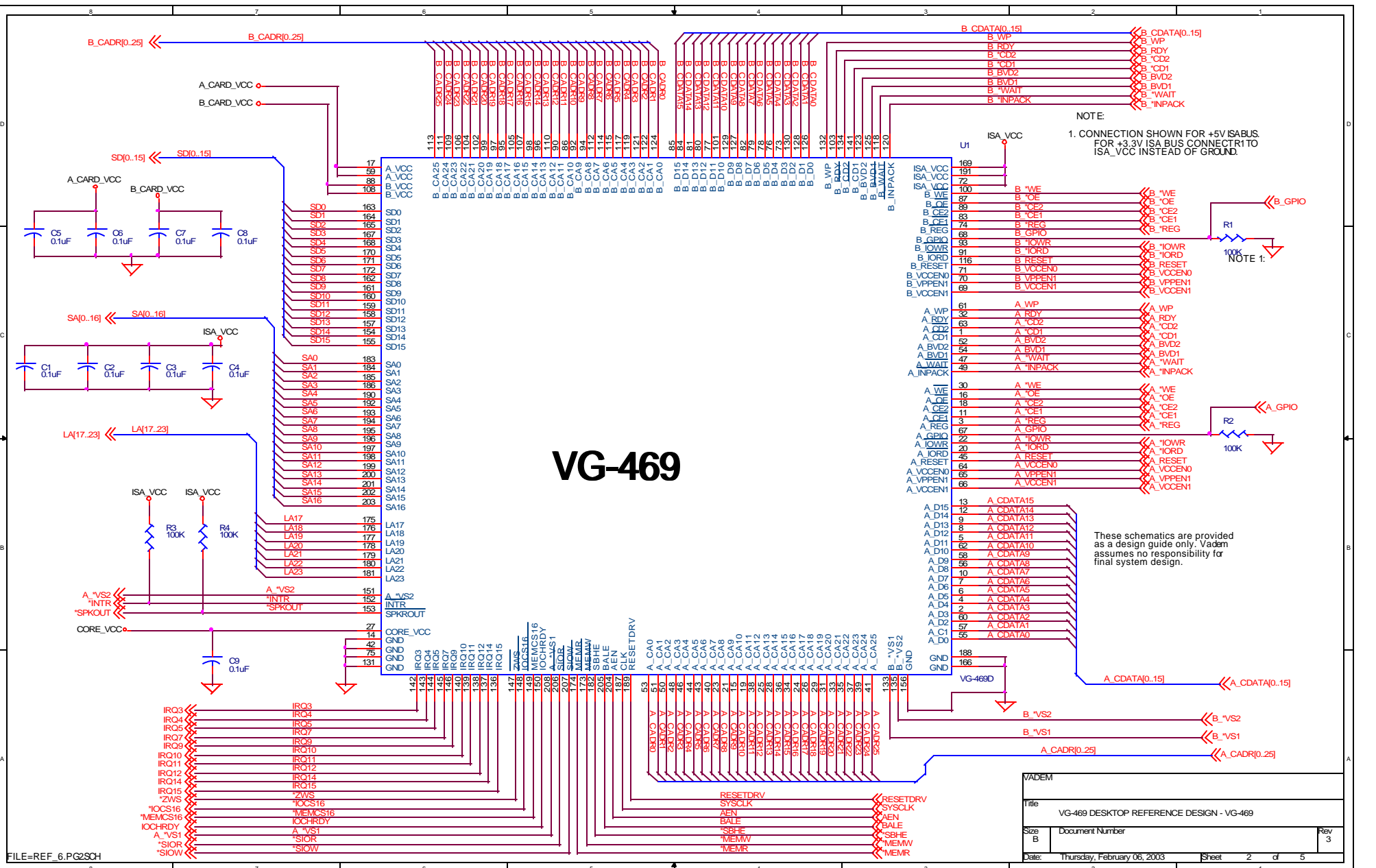
These schematics are provided as a design guide only.
Vadem assumes no responsibility for final system design.

LINK
REF_6PG2.SCH
REF_6PG3.SCH
REF_6PG4.SCH
REF_6PG5.SCH
REF6PG5A.SCH
REF6PG5B.SCH

FILE=469REF_6.SCH

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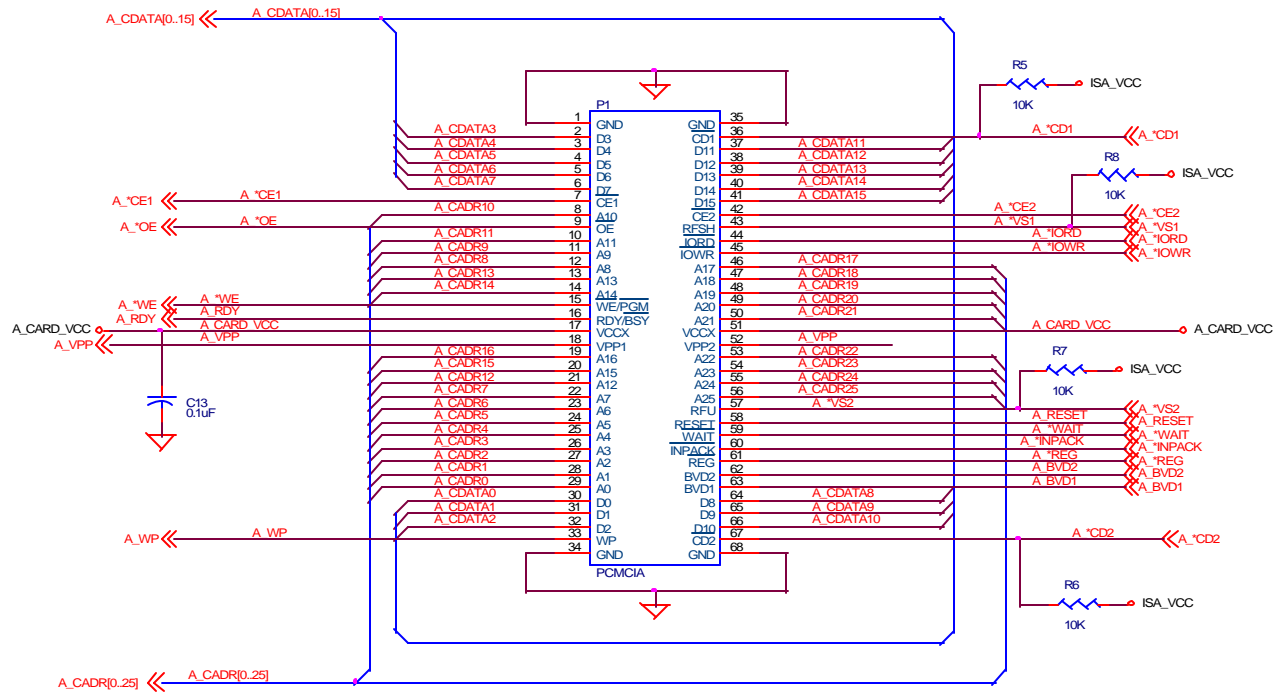
NOTE:
1. CONNECTION SHOWN FOR +5V ISABUS FOR +3.3V ISA BUS CONNECTR1 TO ISA_VCC INSTEAD OF GROUND.

R1
100K
NOTE 1:

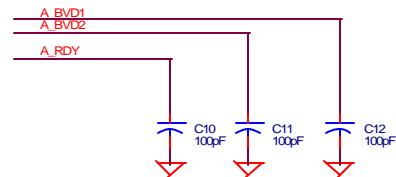
R2
100K

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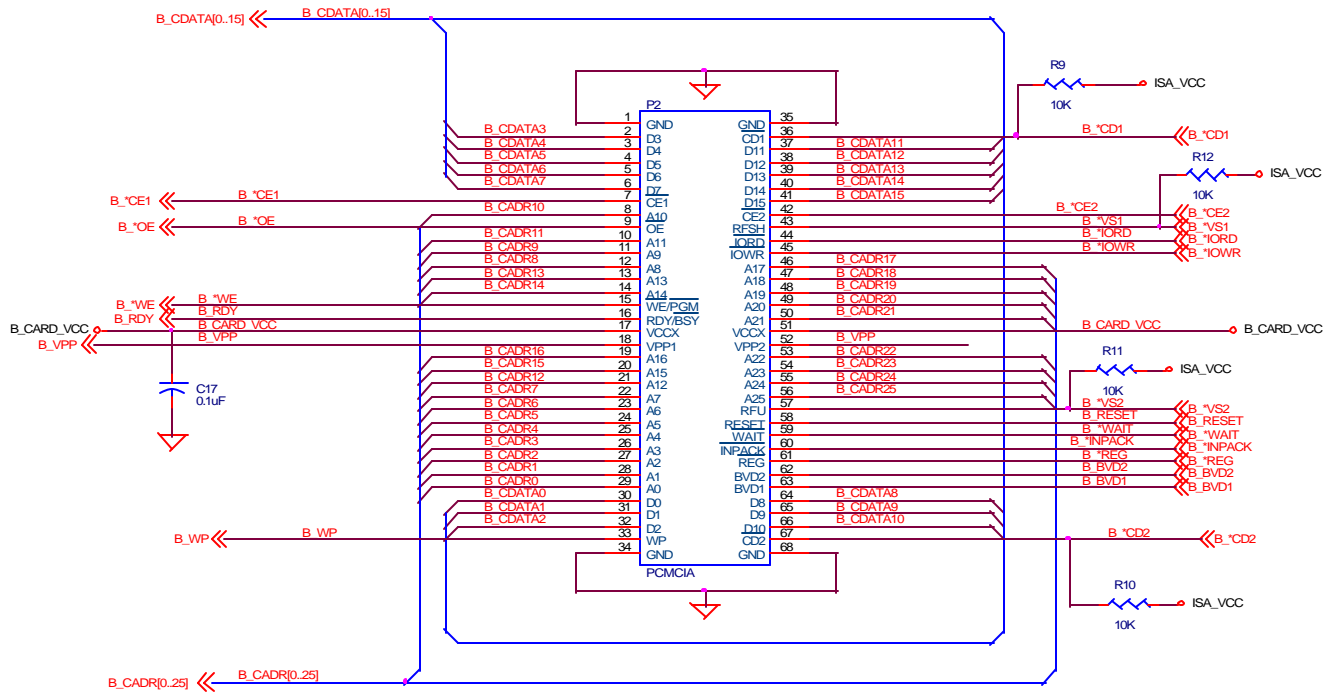
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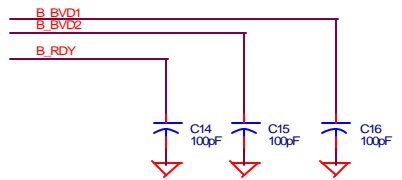
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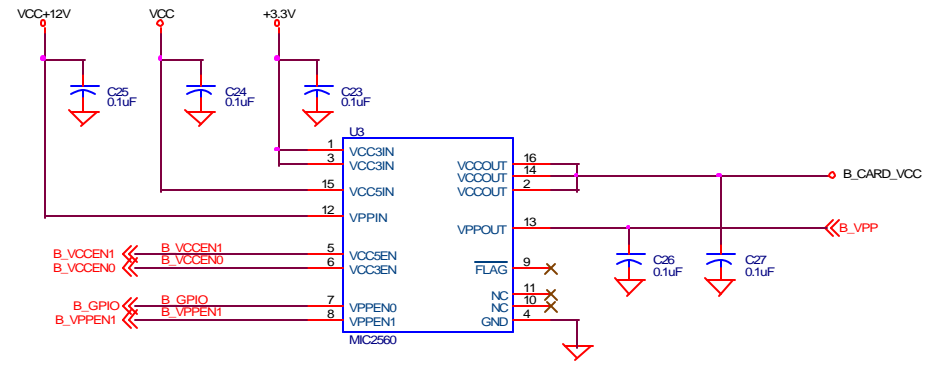
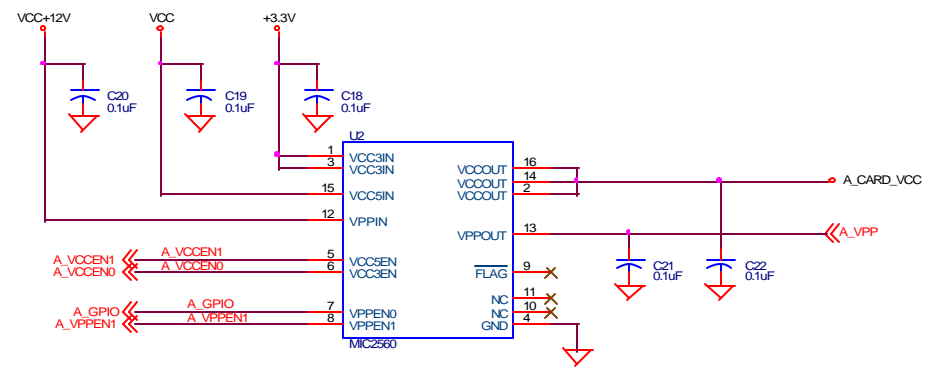
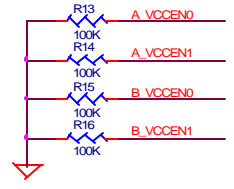


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NOTE:
 THESE RESISTORS SET
 THE "OFF" POLARITY
 OF THE VCC ENABLE
 SIGNALS DURING SYSTEM
 RESET.



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