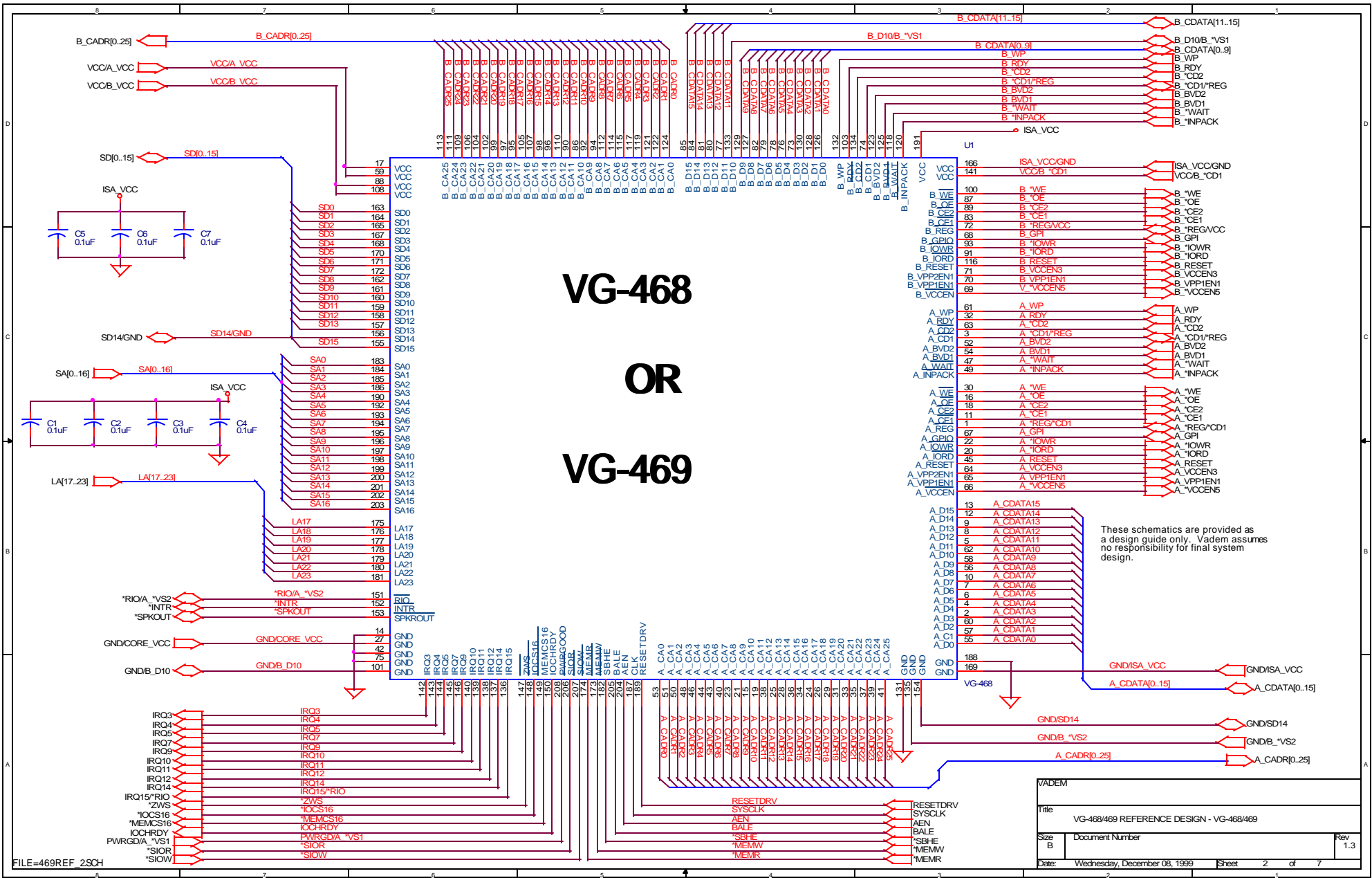
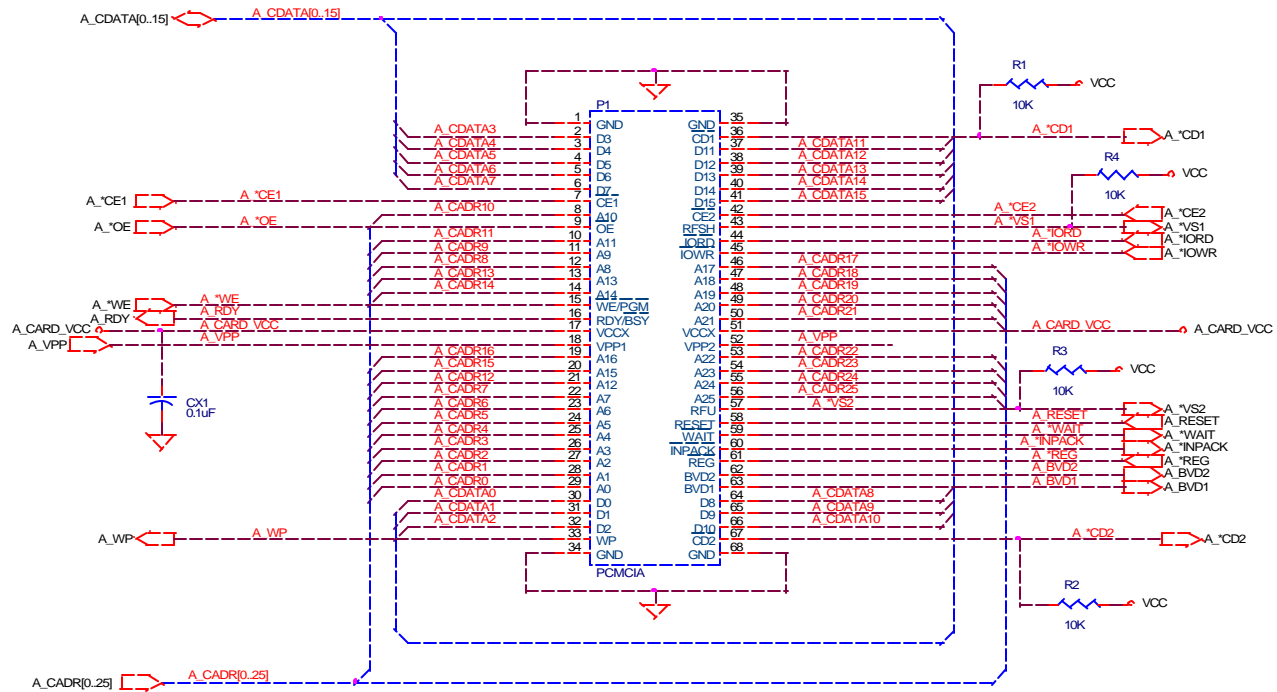


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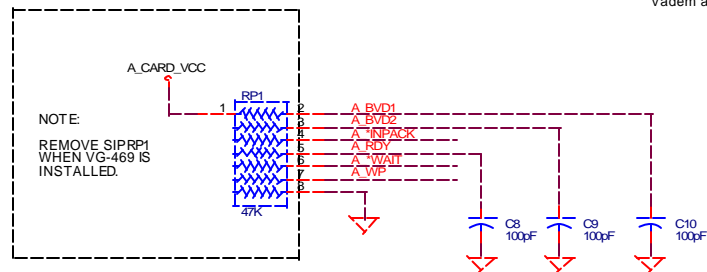
OPTION JUMP ERTABLE			
JUMPER	VG-468	VG-469	COMMENTS
JP1	SHORT	OPEN	
JP2	SHORT	OPEN	
JP3	1-2	2-3	
JP4	1-2	2-3	
JP5	1-2	2-3	
JP6	1-2	2-3	
JP7	2-3	1-2	
JP8	1-2	2-3	
JP9	2-3	2-3	CONNECT ONLY IF PWRGOOD NOT AVAILABLE
JP10	SEE COMMENTS		TEST POINTHEADER
JP11	1-2	2-3	
JP12	1-2	2-3	
JP13	1-2	2-3	
JP14	1-2	2-3	
JP15	1-2	2-3	
JP16	1-2	2-3	
JP17	1-2	2-3	
JP18	1-2	2-3	
JP19	1-2	2-3	
JP20	1-2	2-3	
JP21	SEE COMMENTS		CONNECT 2-3 FOR VG469 USING *RIO, OTHERWISE CONNECT 1-2
JP22	1-2	2-3	
JP23	NOTE	2-3	NOTE: VG-468 1-2 ADDRESS 3E0/1, 2-3 ADDRESS 3E2/3 VG468/469
JP24	SEE COMMENTS		1-2 INDEX 00-FH 2-3 INDEX 80-FH CONNECT 2-3 TO PROGRAM EEPROM, OTHERWISE CONNECT 1-2
JP25	VG-469 ONLY		VG-468 1-2 VG-469 1-2 5V ISA BUS VG-469 2-3 3.3V ISA BUS
JP26	SEE COMMENTS		

VADEM		
Title	VG-468/469 REFERENCE DESIGN	
Size	Document Number	Rev 1.3
Date:	Thursday, February 06, 2003	Sheet 1 of 7



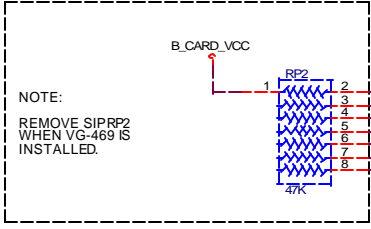
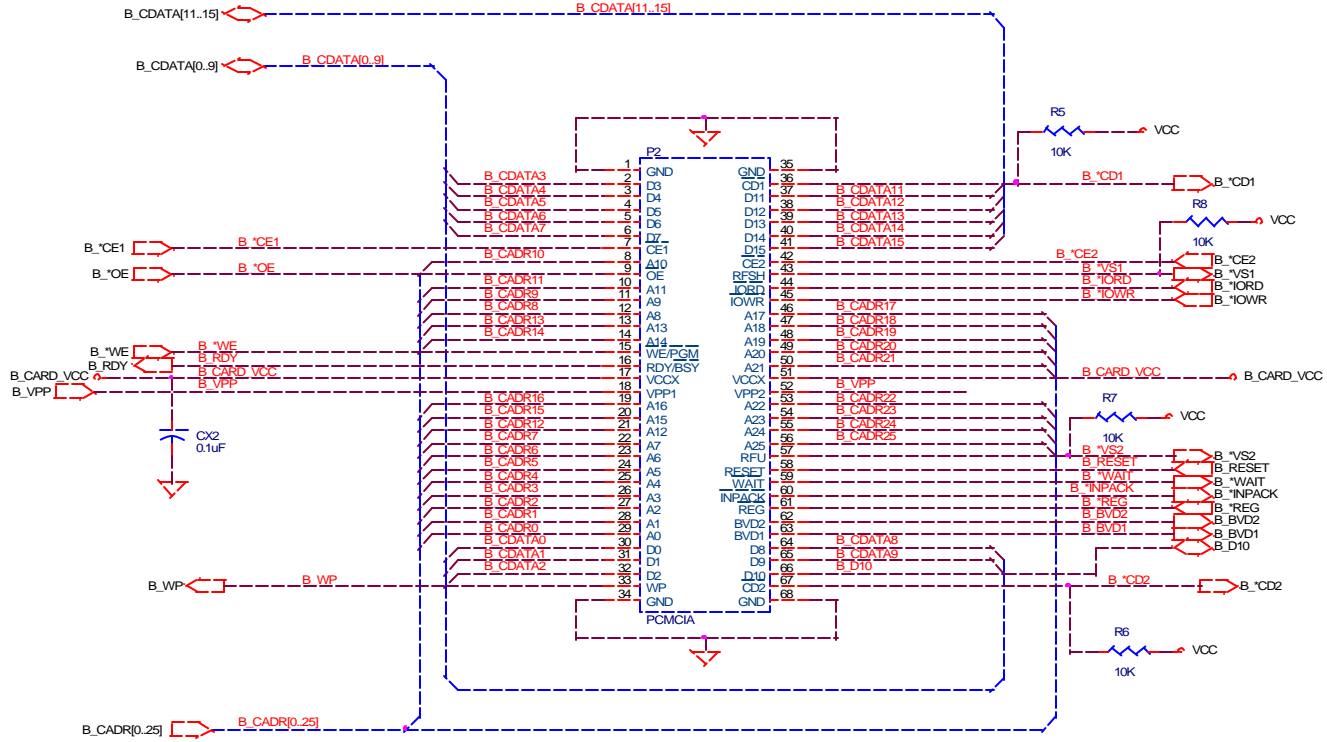


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NOTE:
REMOVE SIPRPI
WHEN VG-469 IS
INSTALLED.

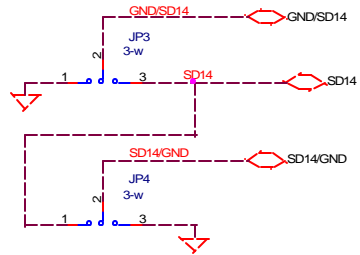
VADEM		
Title VG-468/469 REFERENCE DESIGN - PCMCIA SLOT A		
Size I B	Document Number	Rev 1.3
Date: Wednesday, December 08, 1999	Sheet 3	of 7



NOTE:
REMOVE SIPR2
WHEN VG-469 IS
INSTALLED.

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VAD-EM		
Title VG-468/469 REFERENCE DESIGN - PCMCIA SLOT B		
Size B	Document Number	Rev 1.3
Date: Wednesday, December 08, 1999	Sheet 4	of 7



74HCT04 IS POWERED FROM ISA_VCC.

A_VPP1EN1 → A_VPP1EN1

B_VPP1EN1 → B_VPP1EN1

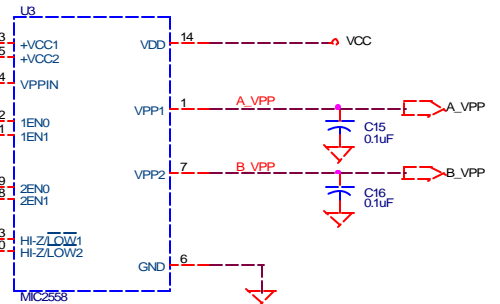
A_CARD_VCC → +VCC1

B_CARD_VCC → +VCC2

VPP → VPPIN

+5V → HI-Z/LOW1

+5V → HI-Z/LOW2



NOTE 1

JP1 JUMPER

JP2 JUMPER

A_VCCEN5 → A_VCCEN5

B_VCCEN5 → B_VCCEN5

+5V

+5V

U2

G1

S1

G2

SIG953

D1A

D1B

D2A

D2B

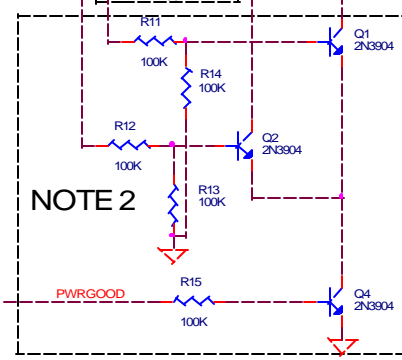
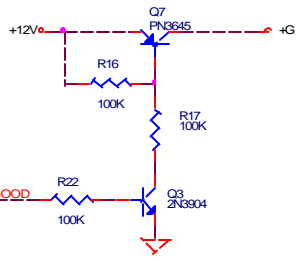
NOTE: JP3 THROUGH JP8 1-2 FOR VG-468, 2-3 FOR VG-469

NOTES

1. INSTALL FOR VG-469 IF ISA_VCC = 5V, AND FOR VG-468.
2. INSTALL FOR VG-469 IF ISA_VCC = 3.3V.
3. INSTALL FOR VG-469 FOR 3.3V CARDSUPPORT

NOTE 2

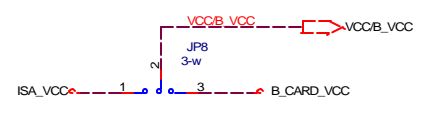
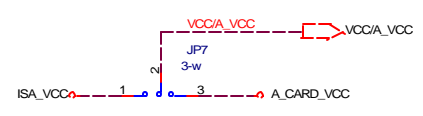
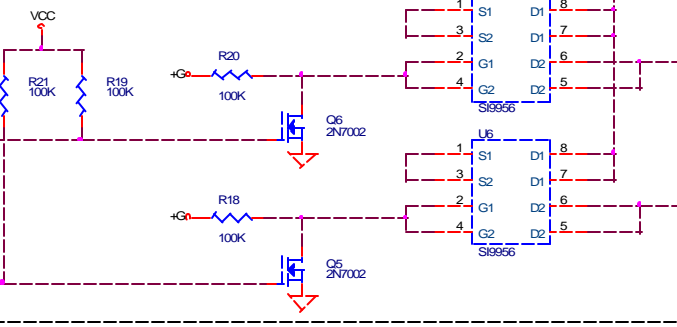
PWRGOOD → PWRGOOD



NOTE 3

A_VCCEN3 → A_VCCEN3

B_VCCEN3 → B_VCCEN3



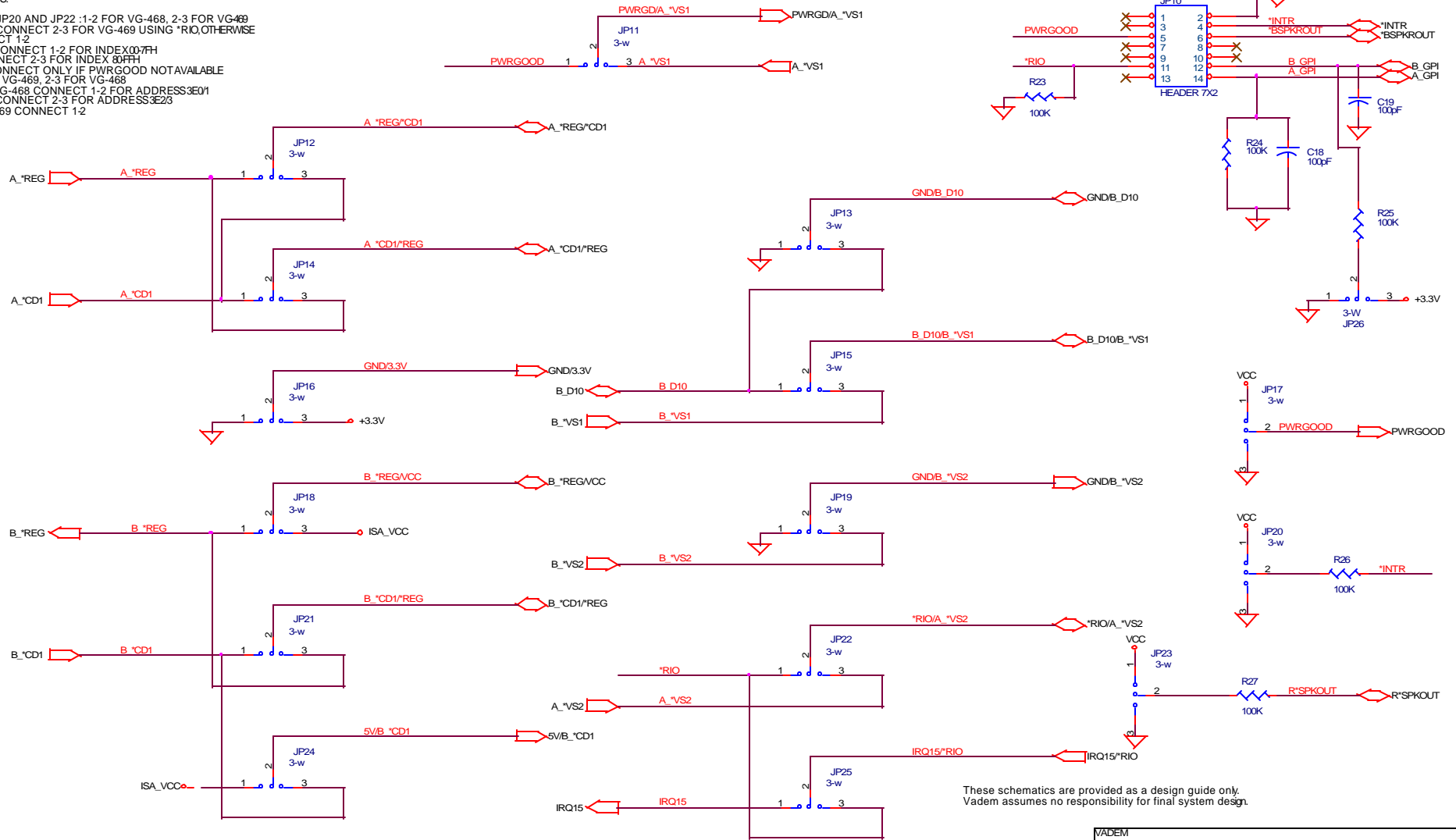
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VAD-EM		
Title VG-468/469 REFERENCE DESIGN - POWER CONTROL		
Size B	Document Number	Rev 1.3
Date: Thursday, February 06, 2003	Sheet 5	of 7

THESE JUMPER OPTIONS ARE ZERO OHM RESISTORS INSTALLED DURING ASSEMBLY.

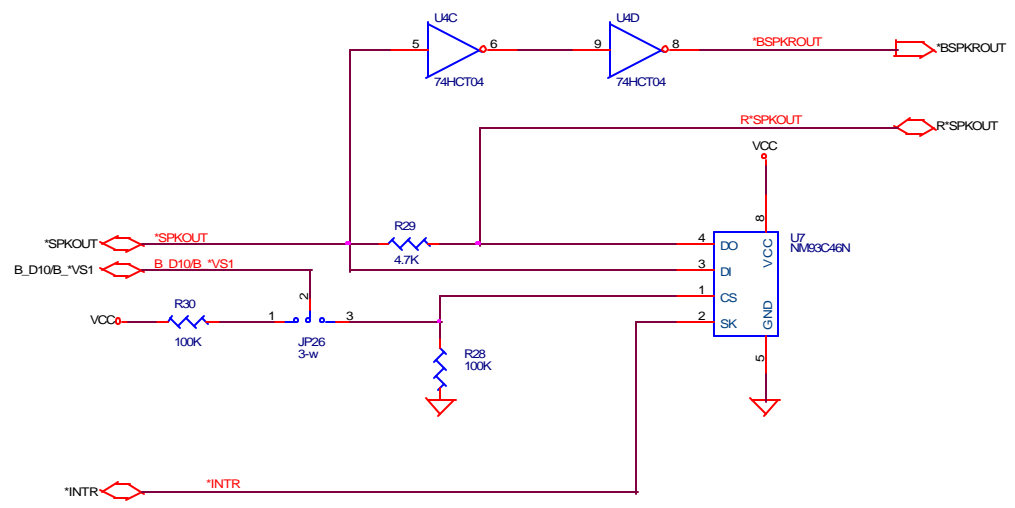
NOTES:

- 1: JP11 - JP20 AND JP22 :1-2 FOR VG-468, 2-3 FOR VG-469
- 2: JP21 - CONNECT 2-3 FOR VG-469 USING *RIO, OTHERWISE CONNECT 1-2
- 3: JP24. CONNECT 1-2 FOR INDEX 007FH
CONNECT 2-3 FOR INDEX 80FFH
- 4: JP9. CONNECT ONLY IF PWRGOOD NOT AVAILABLE
1-2 FOR VG-469, 2-3 FOR VG-468
- 5: JP23. VG-468. CONNECT 1-2 FOR ADDRESS 3E01
CONNECT 2-3 FOR ADDRESS 3E23
VG-469 CONNECT 1-2



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VADEM		
Title: VG-468/469 REFERENCE DESIGN - OPTION JUMPERS		
Size: B	Document Number	Rev: 1.3
Date: Thursday, February 06, 2003	Sheet: 6	of 7



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VADEM		
Title VG-468/469 REFERENCE DESIGN - PLUG & PLAY		
Size B	Document Number	Rev 1.3
Date: Thursday, February 06, 2003	Sheet 7	of 7